

microOBD 200 Pinout

Pin #	Pin Name	Pin Type	Pin Description
1	+5V	P	Positive 5 volt supply input.
2	GND	P	Ground reference for digital logic and drivers (J1962 pin 5).
3	NC	–	Do not connect.
4	VBAT	P	Positive battery voltage supply input (J1962 pin 16).
5	L-LINE	O	ISO9141/ISO14230 L-line output (J1962 pin 15).
6	K-LINE	I/O	ISO9141/ISO14230 bidirectional K-line (J1962 pin 7).
7	$\overline{\text{SLEEP}}$	I	External sleep control input. When enabled in firmware, puts the module into low-power sleep mode. Polarity can be configured in firmware (active low by default). Internal pull-up to +3.3V is enabled by default, but can be disabled in firmware. Leave the pin unconnected if unused.
8	$\overline{\text{LP_OUT}}$	O	Active low output to switch external circuitry into low-power state. This pin has a weak pull-up to +3.3V. It can be pulled up to +5V externally (4 mA max); however, the pull-up will draw current in low-power state. Leave unconnected if unused.
9	UART_RX	I	UART receive input. Compatible with +3.3V and +5V logic.
10	UART_TX	O	UART transmit output. Open drain – requires a pull-up to +3.3V or +5V (4 mA max). Pull-up value depends on UART baud rate and the trace length; typical value is 4.7 k Ω .
11	CAN_L	I/O	CAN Low bidirectional line (J1962 pin 14).
12	CAN_H	I/O	CAN High bidirectional line (J1962 pin 6).
13	$\overline{\text{OBD_TX_LED}} / \overline{\text{RST_NVM}}$	I/O	Active low OBD transmit activity LED output (4 mA max) / active low input to reset NVM to factory defaults. Open drain – pulled up to +3.3V internally. Leave unconnected if unused.
14	$\overline{\text{OBD_RX_LED}} / \overline{\text{INT}}$	O	Active low OBD receive activity LED / interrupt output (4 mA max). Open drain by default (requires a pull-up to +3.3V or +5V when configured as interrupt); can be configured as a +3.3V digital output.
15	$\overline{\text{UART_TX_LED}}$	O	Active low UART transmit activity LED output (4 mA max). Voltage on the anode of the LED must not exceed +3.3V.
16	$\overline{\text{UART_RX_LED}}$	O	Active low UART receive activity LED output (4 mA max). Voltage on the anode of the LED must not exceed +3.3V.
17	+3.3V_OUT	P	Positive 3.3 volt supply output. Maximum available current is 85 mA.
18	AVDD	P	Analog positive supply. Must be connected to +3.3V_OUT or an external voltage reference (between +3.0 and +3.6V).
19	AVSS	P	Ground reference for ANALOG_IN input. Has a point connection to GND. Should be connected to external “clean” analog ground or left unconnected.
20	ANALOG_IN	A	Analog voltage measurement input (+3.3V max). By default, calibrated for an external 62k Ω /10k Ω voltage divider connected to VBAT. Connect to AVSS if unused.
21	NC	–	Do not connect.
22	$\overline{\text{RESET}}$	I	Active low module reset input. Internally pulled up to +3.3V. Leave unconnected if unused.
23	J1850_BUS-	I/O	SAE J1850 Bus- bidirectional line (J1962 pin 10).
24	J1850_BUS+	I/O	SAE J1850 Bus+ bidirectional line (J1962 pin 2).